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STORE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	430873
STORES.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	445867
BUFFER.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	490861
BUFFERS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	124648
ENTRIES.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	56804
ENTRY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	349738
ENTRYS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	62
MULTIPLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	973307
MULTIPLES.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	31917
MULTI.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	661615
MULTIS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	42
(STORE NEAR1 BUFFER NEAR5 ENTRIES AND (MULTIPLE OR MULTI OR PLURAL$5) NEAR5 MATCH AND LOAD).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	13

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Search Results - Record(s) 1 through 3 of 3 returned.

☐ 1. Document ID: US 6223320 B1

L5: Entry 1 of 3   File: USPT   Apr 24, 2001

US-PAT-NO: 6223320
DOCUMENT-IDENTIFIER: US 6223320 B1

TITLE: Efficient CRC generation utilizing parallel table lookup operations

| Full | Title |

☐ 2. Document ID: US 5887183 A

L5: Entry 2 of 3   File: USPT   Mar 23, 1999

US-PAT-NO: 5887183
DOCUMENT-IDENTIFIER: US 5887183 A

TITLE: Method and system in a data processing system for loading and storing vectors
in a plurality of modes

| Full | Title | Citation | Front | Review | Classification |

☐ 3. Document ID: US 5764997 A

L5: Entry 3 of 3   File: USPT   Jun 9, 1998

US-PAT-NO: 5764997
DOCUMENT-IDENTIFIER: US 5764997 A

TITLE: System for generating interrupt requests from either side of an inter-chip
bus

| Full | Title | Citation | Front | Review | Classification | Date | Sequences | KWIC |

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Term	Documents
TARGET.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	414203
TARGETS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	63779
VECTOR$	0
VECTOR.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	199003
VECTORA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	21
VECTORAB.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
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VECTORABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	193
VECTORADD.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	6
VECTORADDS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
"VECTORADD[".DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	3
(L3 AND TARGET NEAR1 VECTOR$).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	3

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Search Results - Record(s) 1 through 20 of 39 returned.

☐ 1.   Document ID: US 20020116595 A1

L6: Entry 1 of 39   File: PGPB   Aug 22, 2002

PGPUB-DOCUMENT-NUMBER: 20020116595
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020116595 A1

TITLE: Digital signal processor integrated circuit

PUBLICATION-DATE: August 22, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Morton, Steven G.	Oxford	CT	US	

US-CL-CURRENT: 712/22

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 2.   Document ID: US 6332186 B1

L6: Entry 2 of 39   File: USPT   Dec 18, 2001

US-PAT-NO: 6332186
DOCUMENT-IDENTIFIER: US 6332186 B1

TITLE: Vector register addressing

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 3.   Document ID: US 6317819 B1

L6: Entry 3 of 39   File: USPT   Nov 13, 2001

US-PAT-NO: 6317819
DOCUMENT-IDENTIFIER: US 6317819 B1

TITLE: Digital signal processor containing scalar processor and a plurality of vector processors operating from a single instruction

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

□ 4. Document ID: US 6304963 B1

L6: Entry 4 of 39   File: USPT   Oct 16, 2001

US-PAT-NO: 6304963
DOCUMENT-IDENTIFIER: US 6304963 B1

TITLE: Handling exceptions occuring during processing of vector instructions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC
Draw Desc	Image										

□ 5. Document ID: US 6282634 B1

L6: Entry 5 of 39   File: USPT   Aug 28, 2001

US-PAT-NO: 6282634
DOCUMENT-IDENTIFIER: US 6282634 B1

TITLE: Apparatus and method for processing data having a mixed vector/scalar register file

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC
Draw Desc	Image										

□ 6. Document ID: US 6266758 B1

L6: Entry 6 of 39   File: USPT   Jul 24, 2001

US-PAT-NO: 6266758
DOCUMENT-IDENTIFIER: US 6266758 B1

TITLE: Alignment and ordering of vector elements for single instruction multiple data processing

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC
Draw Desc	Image										

□ 7. Document ID: US 6247113 B1

L6: Entry 7 of 39   File: USPT   Jun 12, 2001

US-PAT-NO: 6247113
DOCUMENT-IDENTIFIER: US 6247113 B1

TITLE: Coprocessor opcode division by data type

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC
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□ 8. Document ID: US 6223320 B1

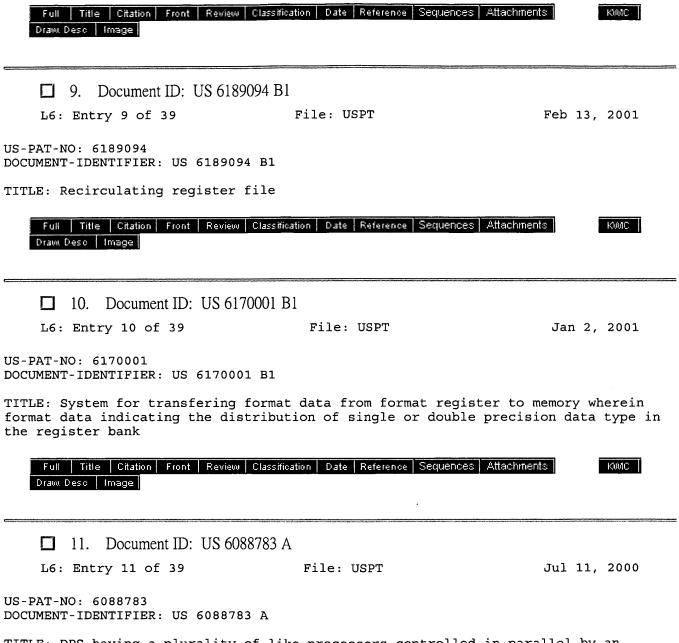L6: Entry 8 of 39   File: USPT   Apr 24, 2001

US-PAT-NO: 6223320
DOCUMENT-IDENTIFIER: US 6223320 B1

TITLE: Efficient CRC generation utilizing parallel table lookup operations

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 9.   Document ID: US 6189094 B1

L6: Entry 9 of 39   File: USPT   Feb 13, 2001

US-PAT-NO: 6189094
DOCUMENT-IDENTIFIER: US 6189094 B1

TITLE: Recirculating register file

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 10.   Document ID: US 6170001 B1

L6: Entry 10 of 39   File: USPT   Jan 2, 2001

US-PAT-NO: 6170001
DOCUMENT-IDENTIFIER: US 6170001 B1

TITLE: System for transfering format data from format register to memory wherein
format data indicating the distribution of single or double precision data type in
the register bank

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 11.   Document ID: US 6088783 A

L6: Entry 11 of 39   File: USPT   Jul 11, 2000

US-PAT-NO: 6088783
DOCUMENT-IDENTIFIER: US 6088783 A

TITLE: DPS having a plurality of like processors controlled in parallel by an
instruction word, and a control processor also controlled by the instruction word

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 12.   Document ID: US 6085275 A

L6: Entry 12 of 39   File: USPT   Jul 4, 2000

US-PAT-NO: 6085275

DOCUMENT-IDENTIFIER: US 6085275 A

TITLE: Data processing system and method thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC

| Draw Desc | Image |

☐ 13. Document ID: US 6058465 A

L6: Entry 13 of 39   File: USPT   May 2, 2000

US-PAT-NO: 6058465
DOCUMENT-IDENTIFIER: US 6058465 A

TITLE: Single-instruction-multiple-data processing in a multimedia signal processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC

| Draw Desc | Image |

☐ 14. Document ID: US 5961628 A

L6: Entry 14 of 39   File: USPT   Oct 5, 1999

US-PAT-NO: 5961628
DOCUMENT-IDENTIFIER: US 5961628 A

TITLE: Load and store unit for a vector processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC

| Draw Desc | Image |

☐ 15. Document ID: US 5887183 A

L6: Entry 15 of 39   File: USPT   Mar 23, 1999

US-PAT-NO: 5887183
DOCUMENT-IDENTIFIER: US 5887183 A

TITLE: Method and system in a data processing system for loading and storing vectors in a plurality of modes

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC

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☐ 16. Document ID: US 5850227 A

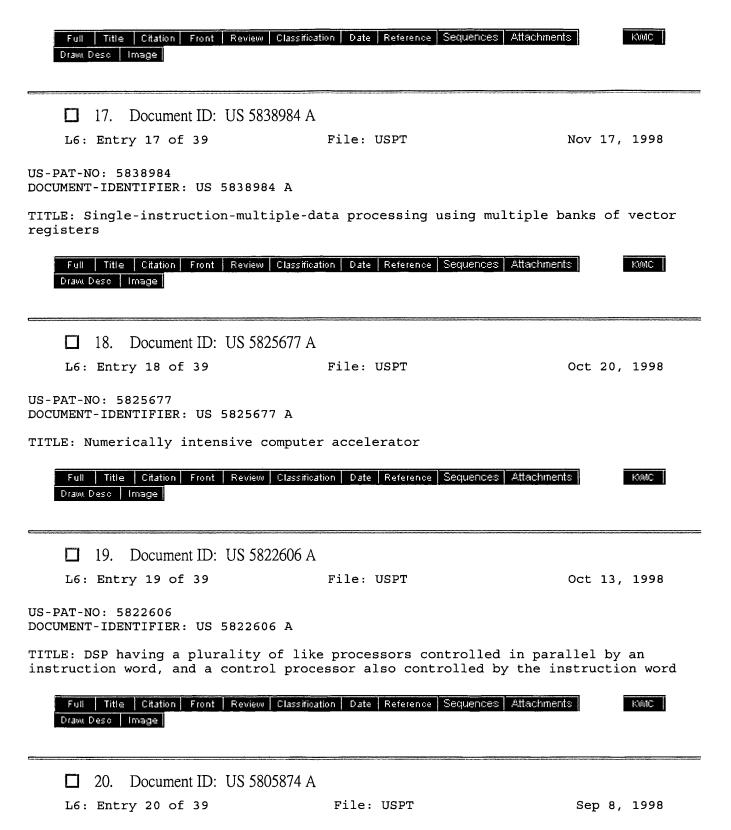L6: Entry 16 of 39   File: USPT   Dec 15, 1998

US-PAT-NO: 5850227
DOCUMENT-IDENTIFIER: US 5850227 A

TITLE: Bit map stretching using operand routing and operation selective multimedia extension unit

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 17. Document ID: US 5838984 A

L6: Entry 17 of 39   File: USPT   Nov 17, 1998

US-PAT-NO: 5838984
DOCUMENT-IDENTIFIER: US 5838984 A

TITLE: Single-instruction-multiple-data processing using multiple banks of vector registers

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 18. Document ID: US 5825677 A

L6: Entry 18 of 39   File: USPT   Oct 20, 1998

US-PAT-NO: 5825677
DOCUMENT-IDENTIFIER: US 5825677 A

TITLE: Numerically intensive computer accelerator

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 19. Document ID: US 5822606 A

L6: Entry 19 of 39   File: USPT   Oct 13, 1998

US-PAT-NO: 5822606
DOCUMENT-IDENTIFIER: US 5822606 A

TITLE: DSP having a plurality of like processors controlled in parallel by an instruction word, and a control processor also controlled by the instruction word

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 20. Document ID: US 5805874 A

L6: Entry 20 of 39   File: USPT   Sep 8, 1998

US-PAT-NO: 5805874
DOCUMENT-IDENTIFIER: US 5805874 A

TITLE: Method and apparatus for performing a vector skip instruction in a data processor

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC
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Term	Documents
ALIGN$6	0
ALIGN.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	227813
ALIGNA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	4
ALIGNABAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABEL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABIE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
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ALIGNABLY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	248
(L4 AND ALIGN$6).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	39

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☐ 21. Document ID: US 5799163 A

L6: Entry 21 of 39   File: USPT   Aug 25, 1998

US-PAT-NO: 5799163
DOCUMENT-IDENTIFIER: US 5799163 A

TITLE: Opportunistic operand forwarding to minimize register file read ports

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 22. Document ID: US 5754805 A

L6: Entry 22 of 39   File: USPT   May 19, 1998

US-PAT-NO: 5754805
DOCUMENT-IDENTIFIER: US 5754805 A

TITLE: Instruction in a data processing system utilizing extension bits and method therefor

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 23. Document ID: US 5752074 A

L6: Entry 23 of 39   File: USPT   May 12, 1998

US-PAT-NO: 5752074
DOCUMENT-IDENTIFIER: US 5752074 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 24. Document ID: US 5742786 A
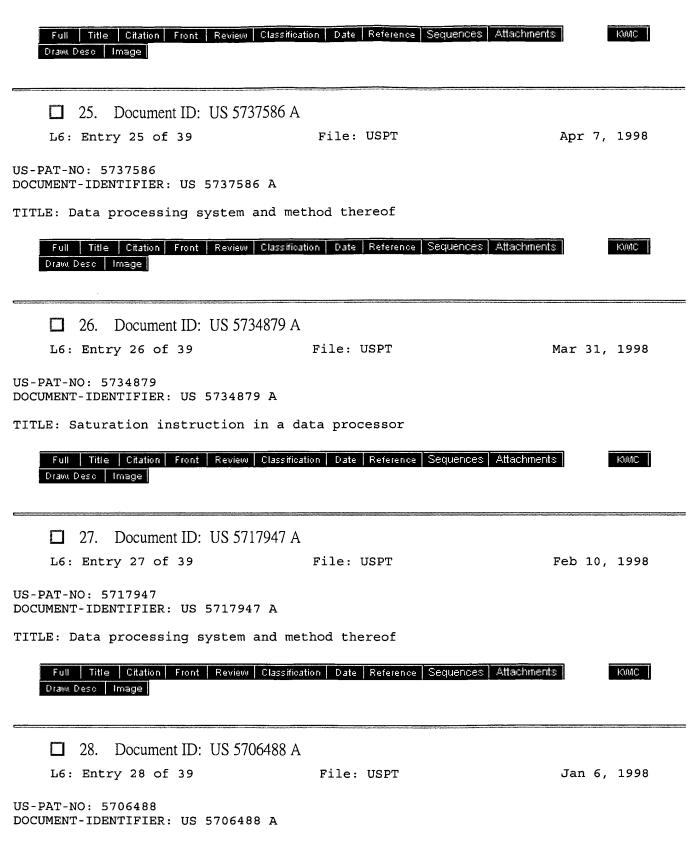
L6: Entry 24 of 39   File: USPT   Apr 21, 1998

US-PAT-NO: 5742786
DOCUMENT-IDENTIFIER: US 5742786 A

TITLE: Method and apparatus for storing vector data in multiple non-consecutive locations in a data processor using a mask value

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 25. Document ID: US 5737586 A

L6: Entry 25 of 39   File: USPT   Apr 7, 1998

US-PAT-NO: 5737586
DOCUMENT-IDENTIFIER: US 5737586 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 26. Document ID: US 5734879 A

L6: Entry 26 of 39   File: USPT   Mar 31, 1998

US-PAT-NO: 5734879
DOCUMENT-IDENTIFIER: US 5734879 A

TITLE: Saturation instruction in a data processor

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 27. Document ID: US 5717947 A

L6: Entry 27 of 39   File: USPT   Feb 10, 1998

US-PAT-NO: 5717947
DOCUMENT-IDENTIFIER: US 5717947 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 28. Document ID: US 5706488 A

L6: Entry 28 of 39   File: USPT   Jan 6, 1998

US-PAT-NO: 5706488
DOCUMENT-IDENTIFIER: US 5706488 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 29. Document ID: US 5673407 A

L6: Entry 29 of 39   File: USPT   Sep 30, 1997

US-PAT-NO: 5673407
DOCUMENT-IDENTIFIER: US 5673407 A

TITLE: Data processor having capability to perform both floating point operations
and memory access in response to a single instruction

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC
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☐ 30. Document ID: US 5664134 A

L6: Entry 30 of 39   File: USPT   Sep 2, 1997

US-PAT-NO: 5664134
DOCUMENT-IDENTIFIER: US 5664134 A

TITLE: Data processor for performing a comparison instruction using selective
enablement and wired boolean logic

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC
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☐ 31. Document ID: US 5600846 A

L6: Entry 31 of 39   File: USPT   Feb 4, 1997

US-PAT-NO: 5600846
DOCUMENT-IDENTIFIER: US 5600846 A

TITLE: Data processing system and method thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC
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☐ 32. Document ID: US 5598571 A

L6: Entry 32 of 39   File: USPT   Jan 28, 1997

US-PAT-NO: 5598571
DOCUMENT-IDENTIFIER: US 5598571 A

TITLE: Data processor for conditionally modifying extension bits in response to data
processing instruction execution

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC
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☐ 33. Document ID: US 5572689 A

US-PAT-NO: 5572689
DOCUMENT-IDENTIFIER: US 5572689 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 34.   Document ID: US 5559973 A

L6: Entry 34 of 39   File: USPT   Sep 24, 1996

US-PAT-NO: 5559973
DOCUMENT-IDENTIFIER: US 5559973 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 35.   Document ID: US 5548768 A

L6: Entry 35 of 39   File: USPT   Aug 20, 1996

US-PAT-NO: 5548768
DOCUMENT-IDENTIFIER: US 5548768 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 36.   Document ID: US 5537562 A

L6: Entry 36 of 39   File: USPT   Jul 16, 1996

US-PAT-NO: 5537562
DOCUMENT-IDENTIFIER: US 5537562 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 37.   Document ID: US 5487022 A

L6: Entry 37 of 39   File: USPT   Jan 23, 1996

US-PAT-NO: 5487022
DOCUMENT-IDENTIFIER: US 5487022 A

TITLE: Normalization method for floating point numbers

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
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☐ 38.   Document ID: US 4945479 A

L6: Entry 38 of 39   File: USPT   Jul 31, 1990

US-PAT-NO: 4945479
DOCUMENT-IDENTIFIER: US 4945479 A

TITLE: Tightly coupled scientific processing system

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
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☐ 39.   Document ID: US 4873630 A

L6: Entry 39 of 39   File: USPT   Oct 10, 1989

US-PAT-NO: 4873630
DOCUMENT-IDENTIFIER: US 4873630 A

TITLE: Scientific processor to support a host processor referencing common memory

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
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Term	Documents
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ALIGNABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	13259
ALIGNABLITY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABLY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	248
(L4 AND ALIGN$6).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	39

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ALIGNA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	4
ALIGNABAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABEL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
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ALIGNABILTY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	13259
ALIGNABLITY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABLY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	248
(L4 AND ALIGN$6).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	39

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L6	L4 and align$6	39	L6
L5	L3 and target near1 vector$	3	L5
L4	L3 and target	60	L4
L3	(simd or single near1 data) and vector$ near6 (destination or source) near5 register$	98	L3
L2	(simd or single near1 data) and vector$ near6 (destination or source)	316	L2
DB=USPT; PLUR=YES; OP=OR			
L1	5671401.pn. or 6035316.pn.	2	L1

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Then click **Search Again**.

simd and (target or destination or source) and vector

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1 **Exploiting a new level of DLP in multimedia applications**
Corbal, J.; Valero, M.; Espasa, R.
Microarchitecture, 1999. MICRO-32. Proceedings. 32nd Annual International
Symposium on , 1999
Page(s): 72 -79

[Abstract]   [PDF Full-Text (80 KB)] **CNF**

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2 **Effective SIMD code generation for the high-level declarative data-language 8 1/2**
De Vito, D.; Michel, O.
EUROMICRO 96. Beyond 2000: Hardware and Software Design Strategies.,
Proceedings of the 22nd EUROMICRO Conference , 1995
Page(s): 114 -119

[Abstract]   [PDF Full-Text (436 KB)] **CNF**

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3 **Data-parallel functional programming**
VanderWiel, S.; Davis, J.A.
Parallel Processing Symposium, 1993., Proceedings of Seventh International
Page(s): 602 -608

[Abstract]   [PDF Full-Text (560 KB)] **CNF**

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4 **A compiler for a massively parallel distributed memory MIMD comp**
Sabot, G.
Frontiers of Massively Parallel Computation, 1992., Fourth Symposium on th
Page(s): 12 -20

[Abstract]   [PDF Full-Text (840 KB)] **CNF**

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